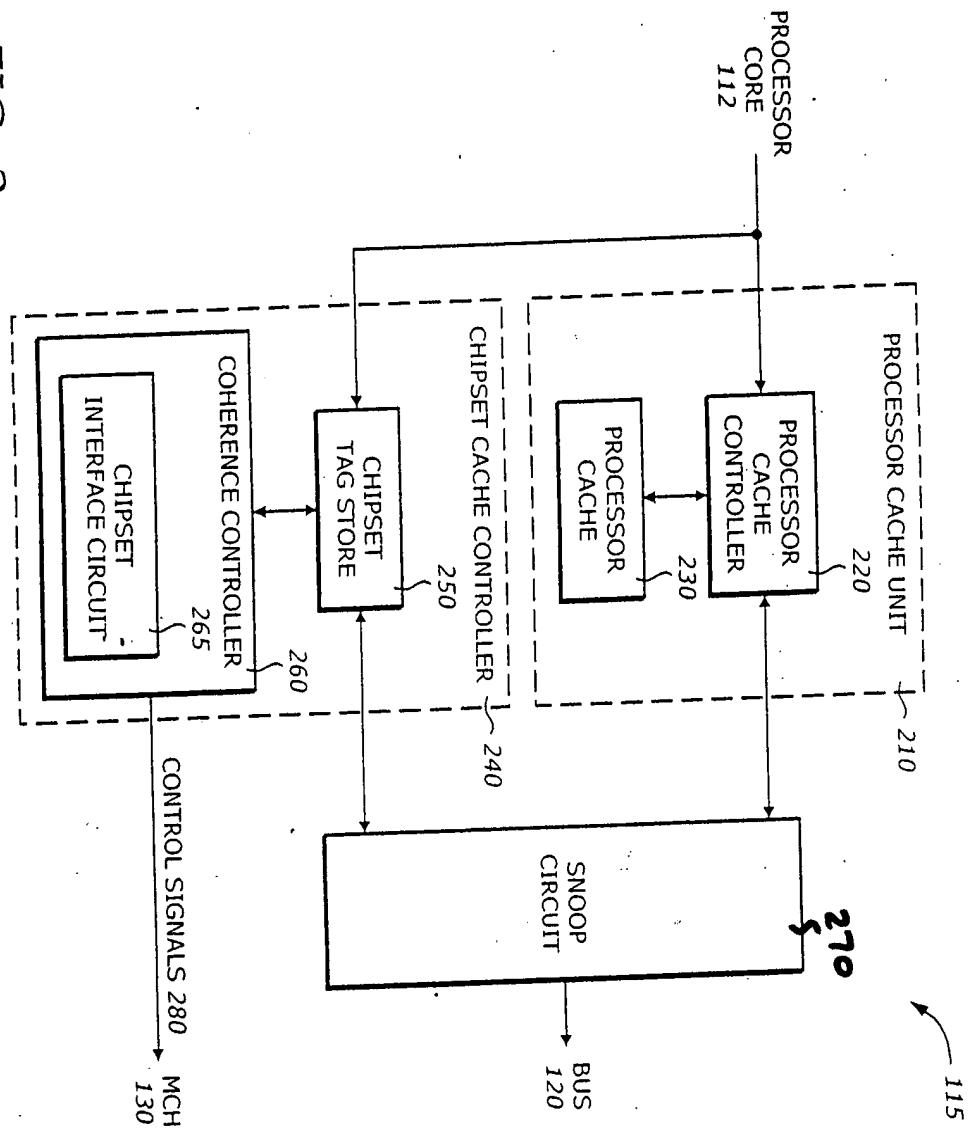




FIG. 2



Blakely, Sokoloff, Taylor & Zafman LLP
 Title: METHOD FOR CONTROLLING A CHIPSET CACHE FROM THE
 PROCESSOR
 1st Named Inventor: Blaise Fanring
 Application No.: 09/622,643
 Docket No.: 42390P10572
 Sheet 2 of 4